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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10006860	FILING DATE 12/04/2001	CLASS 326711	SUBCLASS 156	GAU 288 2919	EXAMINER S. Thomas
**APPLICANTS: Miyamoto Yuichiro; Masuno Takashi; Kuroda Gouki;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-370,543 12/05/2000					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials		PATS-3296US			
TITLE : Asynchronous FIFO circuit and method of reading and writing data through asynchronous FIFO circuit					
U.S. DEPT. OF COM. PAT. & TRADEMARK OFF. 12-01					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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